

Electronic Acknowledgement Receipt

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Application Number:	10728492
International Application Number:	
Confirmation Number:	9752
Title of Invention:	Low power memory controller with leaded double data rate DRAM package arranged on a two layer printed circuit board
First Named Inventor/Applicant Name:	Eric Hung
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		ROA_1-29-07.pdf	127040	yes	14
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment - After Non-Final Rejection		1	1	
	Specification		2	3	
	Claims		4	8	
	Applicant Arguments/Remarks Made in an Amendment		9	14	
Warnings:					
Information:					
Total Files Size (in bytes):			127040		
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